

Commonly Used Acronyms

A	Amperes	CLA	Carry Look-ahead Adder
AC	Alternating Current	CMOS	Complementary Metal-Oxide Semiconductor
A/D	Analog to Digital	CP/M	Control Program / Monitor
ADC	Analog to Digital Converter	CPI	Clocks Per Instruction
AE	Applications Engineer	CPLD	Complex Programmable Logic Device
AI	Artificial Intelligence	CPU	Central Processing Unit
ALU	Arithmetic-Logic Unit	CR	Carriage Return
AM	Amplitude Modulation	CRC	Cyclic Redundancy Code
AMD	Advanced Micro Devices, Inc.	CRQ	Command Response Queue
ANSI	American National Standards Institute	CRT	Cathode Ray Tube
ARQ	Automatic Retransmission reQuest	CS	Chip Select / Check-Sum
ASCII	American Standard Code for Information Interchange	CSMA	Carrier Sense Multiple-Access
ASEE	American Society for Engineering Education	CSMA/CD	Carrier Sense Multiple-Access with Collision Detect
ASIC	Application Specific Integrated Circuit	CSR	Command Status Register
ASPI	Advanced SCSI Programming Interface	CTS	Clear To Send
ATDM	Asynchronous Time Division Multiplexing	D	Dissipation Factor
ATM	Asynchronous Transfer Mode	D/A	Digital to Analog
AUI	Attached Unit Interface	DAC	Digital to Analog Converter
B	Magnetic Flux	DAT	Digital Audio Tape
BBS	Bulletin Board System	dB	(DB) deciBels
BCC	Block Check Character	dBm	dB referenced to 1 milliWatt
BCD	Binary Coded Decimal	DC	Direct Current
BiCMOS	Bipolar Complementary Metal-Oxide Semiconductor	DCD	Data Carrier Detect
BIOS	Basic Input / Output System	DCE	Data Circuit (Channel) Equipment
BNC	Bayonet Nut(?) Connector	DD	Double Density
BPS/bps	Bytes/bits Per Second	DDD	Direct Distance Dialing
BSC	Binary Synchronous Communications	DEC	Digital Equipment Corporation
BSD	Berkeley Standard Distribution	DES	Data Encryption Standard
C	Capacitance	DID	Direct Inward Dial
CAD	Computer Aided Design	DIN	Deutsche Industrie Norm
CAM	Computer Aided Manufacturing	DIP	Dual-In-line Package
CAM	Content Addressable Memory	DMA	Direct Memory Access
CAS	Column Address Strobe	DOS	Disk Operating System
CBX	Common Branch eXtender	DPDT	Double-Pole Double-Throw (switch)
CCD	Charge Coupled Device	DPE	Data Parity Error
CCITT	Consultative Committee of International Telephony and Telegraphy	DPSK	Differential Phase Shift Keying
CD	Carrier Detect	DRAM	Dynamic Random Access Memory
CDDI	Copper Distributed Data Interface	DS	Double Sided
CDROM	Compact Disk Read Only Memory	DSP	Digital Signal Processor
CDMA	Code Division Multiple Access	DSR	Data Set Ready
CGA	Color Graphics Adapter	DTC	Data Terminal Controller
CISC	Complex Instruction-Set Computer	DTE	Data Terminal (Terminating) Equipment
		DTMF	Dual-Tone Multi-Frequency
		DTR	Data Terminal Ready

DVD	Digital Video Disk	GAs	Gallium Arsenide
		GFLOPS	Billions (10^9) of FLOating Point Operations Per Second ("Giga-Flops")
		GHz	Giga Hertz
		GNU	Gnu's Not Unix
		GPIB	General Purpose Interface Bus
E	EMF	GUI	Graphical User Interface
EBCDIC	Extended Binary Coded Decimal Interchange Code	H	Henries
ECC	Error Correction Code	H	Magnetic Force
ECL	Emitter-Coupled Logic	HD	High Density
ECN	Engineering Change Notice	HDD	Hard Disk Drive
ECO	Engineering Change Order	HDX	Half-Duplex Transmission
ECR	Engineering Change Request	HFS	Hierarchical File System
EEPROM	Electrically Erasable Programmable Read-Only Memory	HP	Hewlett-Packard
EGA	Enhanced Graphics Adapter	I	Current
EIA	Electronic Industries Association	HPIB	Hewlett-Packard Interface Bus
EISA	Enhanced Industry Standard Architecture	I/O	Input / Output
EMI	Electro-Magnetic Interference	IBM	International Business Machines Corp.
EMF	Electro-Motive Force	IC	Integrated Circuit
EMS	Expanded Memory Specification	IDC	Insulation Displacement Connector
EOF	End Of File	IDE	Integrated Device Electronics
EOL	End Of Line	IEEE	Institute of Electrical and Electronic Engineers
EPROM	Erasable Programmable Read-Only Memory	IMP	Interface Message Processor
ESD	Electro-Static Discharge	IP	Internet Protocol
ESDI	Enhanced Small Devices Interface	IPC	InterProcess Communication
F	Farads	IRQ	Interrupt ReQuest
FAT	File Allocation Table	ISA	Industry Standard Architecture
FCC	Federal Communications Commission	ISDN	Integrated Services Digital Network
FDD	Floppy Disk Drive	ISO	International Standards Organization
FDDI	Fiber Distributed Data Interface	ISP	ISP Internet Service Provider
FDM	Frequency Division Multiplexing		
FDMA	Frequency Division Multiple Access	J	Joules of energy
FDX	Full-Duplex Transmission	K	Kilo
FE	Front End	KVA	Kilo Volt-Amps
FEP	Front End Processor	LAN	Local Area Network
FF	Form Feed	LAP	Link Access Protocol
FF	Flip-Flop	LAPB	Link Access Protocol Balanced
FFT	Fast Fourier Transform	LCD	Liquid Crystal Display
FIFO	First-In First-Out	LED	Light Emitting Diode
FILO	First-In Last-Out	LF	Line Feed
FLOPS	Floating-point Operations Per Second	LIFO	Last In First Out
FM	Frequency Modulation	LSB	Least Significant Bit (or Byte)
FPGA	Field Programmable Gate Array	LSI	Large Scale Integration
FPU	Floating Point Unit	LUN	Logical Unit Number
FRU	Field-Replaceable Unit		
FSK	Frequency Shifty Keying	M	Mega
FTP	File Transfer Program	m	milli
		MAN	Metropolitan Area Network
		MB	Mega Bytes
		MBR	Master Boot Record
		MCA	Micro Channel Architecture
		MCGA	Multi-Color Graphics Array

MCM Multi-Chip Module
 mfd micro Farads
 MFLOPS Millions of FLOating Point Operations per Second ("MegaFlops")
 MFM Modified Frequency Modulated
 MHz MegaHertz
 MIDI Musical Instrument Digital Interface
 MIPS Millions of Instructions per Second
 MMU Memory Management Unit
 MNP Microcom Network Protocol
 MODEM MOdulator / DEModulator
 MOPS Millions of Operations Per Second
 MOS Metal-Oxide Semiconductor
 MSB Most Significant Bit (or Byte)
 MSDOS Microsoft Disk Operating System
 MSI Medium Scale Integration
 MTBF Mean Time Between Failures

 n nano
 NAND Not And
 N/C No-Connect
 NBS National Bureau of Standards
 NEMA National Electrical Manufacturers Association
 NFS Network File System
 NIC Network Interface Card
 NIST National Institute of Standards and Technology
 NMI Non-Maskable Interrupt
 NMOS Negatively doped Metal-Oxide Semiconductor
 NOP No OPERATION
 NSF National Science Foundation
 NVRAM NonVolatile Random Access Memory
 OCR Optical Character Recognition
 ODI Open Datalink Interface
 OEM Original Equipment Manufacturer
 OS Operating System
 OSF Open Software Foundation
 OSI Open Systems Interconnect
 p pico
 PAL Programmable Array Logic
 PLA Programmable Logic Array
 PB Push Button
 PBX Private Branch eXchange
 PC Personal Computer, Program Counter
 PCB Printed Circuit Board
 PCI Peripheral Component Interconnect
 PCM Pulse Code Modulation
 PCMCIA Personal Computer Memory Card International Association
 PE Professional Engineer
 PGA Pin Grid Array

PIA Peripheral Interface Adapter
 PIC Programmable Interrupt Controller
 PIO Programmed Input/Output
 PLCC Plastic Leaded Chip Carrier
 PLD Programmable Logic Device
 PLL Phase Locked Loop
 PMOS Positively doped Metal-Oxide Semiconductor
 POP Post Office Protocol (email)
 POST Power On Self Test
 POTS Plain Old Telephone Service
 PPP Point-to-Point Protocol
 PQFP Plastic Quad-FlatPack
 PROM Programmable Read-Only Memory
 PSTN Public Switched Telephone Network

 Q Charge
 QAM Quadrature Amplitude Modulation
 QFP Quad-FlatPack
 R Resistance
 RAM Random Access Memory
 RAS Row Address Strobe
 RCA Radio Corporation of America
 R/C Radio Control
 RC Resistor - Capacitor
 RF Radio Frequency
 RFC Radio Frequency Choke
 RFI Radio Frequency Interference
 RFP Request For Proposal
 RFQ Request For Quotation
 RI Ring Indicator
 RISC Reduced Instruction-Set Computer
 RLL Run Length Limited
 RMS Root Mean Square
 ROM Read-Only Memory
 RPM Revolutions Per Minute
 RS Recommended Specification
 RTC Real Time Clock
 RTS Request To Send

 S Siemens
 SASI Shugart Associates Standard Interface
 SCSI Small Computer Systems Interface
 SD Single Density
 SDLC Synchronous Data Link Control
 SIMM Single Inline Memory Module
 SIP Single Inline Package
 SLIP Serial Line Internet Protocol
 SMD Surface Mount Device
 SMT Surface Mount Technology
 SNA System Network Architecture
 SNR Signal to Noise Ratio

SOT	Small Outline Transistor	XOR	Exclusive-Or
SOIC	Small Outline Integrated Circuit	Y	Admittance
SPOOL	Simultaneous Peripheral Operation On Line	Z	Impedance
SPST	Single-Pole Single-Throw (switch)	ZIF	Zero Insertion Force
SPT	Sectors Per Track		
SQE	Signal Quality Error		
SRAM	Static Random Access Memory		
SS	Single Sided		
STDM	Synchronous Time Division Multiplexing		
STN	Super Twisted Nematic		
STP	Shielded Twisted Pair		
STU	Streaming Tape Unit		
SVGA	Super Video Graphics Array		
TCM	Trellis Code Modulation		
TCP/IP	Transmission Control Protocol / Internet Protocol		
TDM	Time Division Multiplexing		
TDMA	Time Division Multiple Access		
TI	Texas Instruments		
TIA	Telecomm. Industry Association		
TPI	Tracks Per Inch		
TSR	Terminate and Stay Resident		
TTL	Transistor-Transistor Logic		
TUV	Technischer Ueberwachuags Verein		
UV	Ultra Violet		
UART	Universal Asynchronous Receiver/Transmitter		
UDP	User Datagram Protocol		
UMB	Upper Memory Block		
UPS	Uninterruptible Power Supply		
UTP	Unshielded Twisted Pair		
UUCP	Unix to Unix Copy Program		
VCR	Video Cassette Recorder		
VESA	Video Enhanced Standards Association		
VGA	Video Graphics Array		
VLB	VESA Local Bus		
VLSI	Very Large Scale Integration		
VM	Virtual Memory		
VME	Versa Module Eurocard		
VRAM	Video Random Access Memory		
VTR	Video Tape Recorder		
W	Watts		
WAN	Wide Area Network		
WATS	Wide Area Telephone Service		
WORM	Write-Once Read-Many		
WWW	World Wide Web		
XGA	eXtended Graphics Array		
XMS	Extended Memory Specification		